

### **REMARKS**

Claims 6-14, 17-27, 29 and 47-50 were pending in this application. By this amendment, claim 6 is amended, and new claims 55-59 are added. Thus, claims 6-14, 17-27, 29, 47-50, and 55-59 are now pending in this application. No new matter has been added. In view of the above amendments, and the following remarks, Applicants respectfully request reconsideration and allowance of the present application.

In the office action, the Examiner asserts that the oath or declaration is defective since it “fails to list and claim the foreign priority which was claimed in the issued Patent.” In response, Applicants submit herewith an updated Reissue Application Declaration and a Supplemental Priority Data Sheet that correctly includes the foreign priority information, as well as a listing of the foreign priority applications.

The Examiner also asserts that the reissue oath/declaration filed with this application is defective because Applicant has cancelled the claims for which the error was originally presented. Thus, the Examiner asserts that an error no longer exists within the reissue application that is being corrected, and that the reissue application cannot issue with no error, i.e., we can’t have a no-error reissue application. However, new method claims 55-59 address the error upon which this reissue application was based.

In particular, independent claim 55 (which is identical to originally filed method claim 30) recites a method of operating an active matrix display device comprising the steps of storing a data in a memory circuit provided at one pixel; and supplying a voltage to a pixel electrode of said pixel in accordance with the data stored in said memory circuit, wherein said memory circuit comprises at least first and second inverters, each inverter comprising one p-channel type thin film transistor and one n-channel type thin film transistor formed over a substrate.

In addition, independent claim 57 (which is based on originally filed method claim 32) recites a method of operating an active matrix display device comprising the steps of supplying a data through a switching thin film transistor provided at one pixel to a memory circuit; storing

said data in a memory circuit provided at said pixel; and supplying one of two voltages from two voltage source lines to a pixel electrode of said pixel in accordance with the data stored in said memory circuit wherein said two voltage source lines are electrically connected to said memory circuit, wherein said memory circuit comprises at least first and second inverters, each inverter comprising one p-channel type thin film transistor and one n-channel type thin film transistor formed over a substrate. Thus, Applicants respectfully submit that this reissue now addresses the error stated in the declaration. Accordingly, this objection is believed to be overcome.

Claims 6-14, 17-27, 29 and 47-50 stand rejected as being based upon a defective reissue oath/declaration under 35 U.S.C. § 251 as set forth above. However, in view of the addition of new method claims 55-59 herein, and in view of the updated declaration, Applicants respectfully submit that pending claims 6-14, 17-27, 29, 47-50, and 55-59, are now proper.

Claim 6 stands rejected as having insufficient antecedent basis for the limitation "said pixel electrode" in line 15. However, Applicants respectfully submit that amended claim 6 as presented herein has sufficient antecedent basis. Thus, Applicants request that this rejection be withdrawn.

In view of the foregoing, it is submitted that the present application is in condition for allowance and a notice to that effect is respectfully requested. If, however, the Examiner deems that any issue remains after considering this response, the Examiner is invited to contact the undersigned attorney to expedite the prosecution and engage in a joint effort to work out a mutually satisfactory solution.

**Except** for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§ 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account No. 19-2380. This paragraph is intended to be a **CONSTRUCTIVE PETITION FOR EXTENSION OF TIME** in accordance with 37 C.F.R. § 1.136(a)(3).

Respectfully submitted,

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